

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Washington, D.C. 20231.

Patent Application

Applicant(s): A.D. Baker et al.

Case:

19-3

Serial No.:

09/484,098

Filing Date:

January 18, 2000

Group:

2157

Examiner:

Gregory G. Todd

Title:

Methods and Apparatus for Local Network

Address Acquisition, Analysis and Substitution

TRANSMITTAL OF FORMAL DRAWINGS

Assistant Commissioner for Patents Washington, D.C. 20231

Attention: Official Draftsperson

RECEIVED

I hereby certify that this paper is being deposited on this date with the U.S. Postal

Service as first class mail addressed to the Assistant Commissioner for Patents,

FEB 2 1 2003

Technology Center 2100

Sir:

Applicants submit herewith two (2) sheets of formal drawings in the above-referenced patent application.

Respectfully submitted,

Date: February 12, 2003

Joseph B. Ryan

Reg. No. 37,922

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90 Forest Avenue

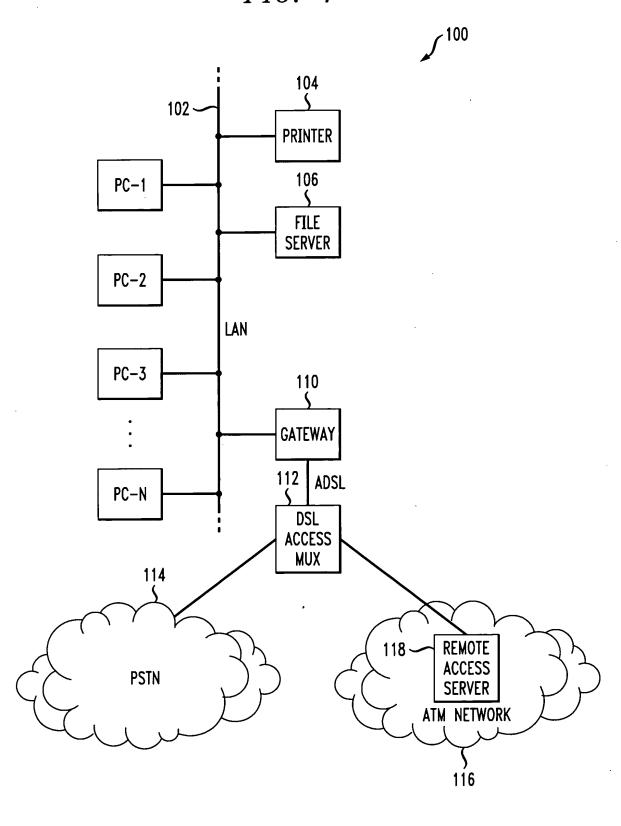
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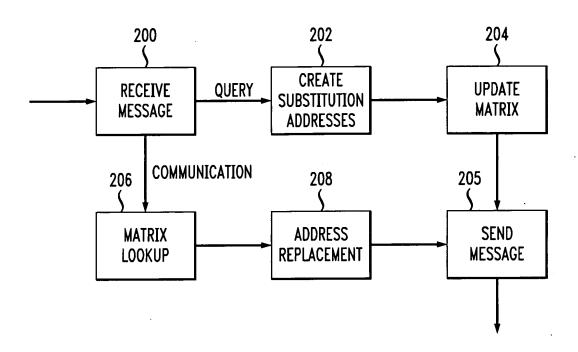
FIG. 1





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FIG. 2



GATEWAY

PROCESSOR

306

LAN
INTERFACE

BUS
ATM/DSL
INTERFACE

MEMORY